

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re patent application of:

Sitaram YADAVALLI, et al.

Serial No.:

09/531,910

Group Art Unit:

2123

Filed:

March 20, 2000

Examiner:

H. Day

FOR:

METHOD AND APPARATUS FOR MODELING AND

CIRCUITS WITH ASYNCHRONOUS BEHAVIOR

## AMENDMENT AND RESPONSE UNDER 37 C.F.R. § 1.111

RECEIVED

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450 OCT 3 0 2003

**Technology Center 2100** 

Sir:

In response to the Office Action mailed July 16, 2003, for which the response period is extended one month by the attached petition for extension of time, please amend the above-identified application as follows: